

7

Atty. Dkt. No. 039153-0642 (H0970)

CERTIFICATE OF EXPRESS MAILING

I hereby certify that this correspondence is being deposited with the
United States Postal Service's "Express Mail Post Office To

Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Commissioner for Patents, P.O. Box

Roberta A. Cooper

(Printed Name)

11/22/04

(Date of Deposit)

1450, Alexandria, VA 22313-1450.

(Express Mail Label Number)

EV 505576442 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Paton et al.

Title:

OFFSET SPACER

PROCESS FOR FORMING N-TYPE TRANSISTORS

(formerly "OFFSET

SPACER SMOS PROCESS FOR FORMING N-TYPE

TRANSISTORS")

Appl. No.:

10/619,877

Filing Date:

07/15/2003

Examiner:

Christy L. Novacek

Art Unit:

2822

Confirmation No.:

7818

AMENDMENT AND REPLY UNDER 37 CFR 1.116

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

This communication is responsive to the Final Office Action dated September 23, 2004, concerning the above-referenced patent application.

Amendments to the Title are reflected on page 2 of this document.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this document.

Remarks/Arguments begin on page 8 of this document.

Please amend the application as follows:

11/24/2004 HALI11

00000038 10619877

01 FC:1202 02 FC:1201 18.00 OP 176.00 OP

-1-

Amendments to the Title:

Please delete the Title of the Application and replace it with the following title: OFFSET SPACER PROCESS FOR FORMING N-TYPE TRANSISTORS